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				<i>Complete if Known</i>	
				Application Number	10/749,035
				Filing Date	December 30, 2003
				First Named Inventor	Sydir, Jaroslaw
				Art Unit	2135
				Examiner Name	Patel, Nirav
Sheet	1	of	1	Attorney Docket No: Intel-012PUS	

US PATENT DOCUMENTS					
Examiner Initial *	Cite No	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Filing Date if Appropriate

FOREIGN PATENT DOCUMENTS					
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		YOA, A. et al., "Speculation Techniques for Improving Load Related Instruction Scheduling", IEEE, (1999).	

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached